

Abstract

Data processing device

The invention provides a data processing device with a multiplicity of processors (P1-P5) which operate in parallel and to which a respective command (CMP1, CMP2, MOV1, MOV2, FSEL) for data processing can be supplied simultaneously. At least one of the processors (P5) can be supplied with a condition command (FSEL) which makes the execution of a further command (CMP1, CMP2, MOV1, MOV2) in at least one of the further processors (P1-P4) conditional on the condition command (FSEL).

Fig. 1